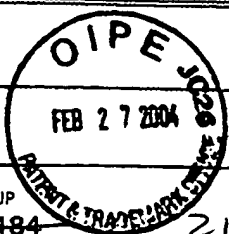


FORM PTO 1449 (modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary) Date Submitted to PTO: February 26, 2004		ATTY DOCKET NO. PD-203009	APPLICATION NO. 10/613,824	
APPLICANT M. EROZ et al.		FILING DATE July 3, 2003	GROUP 2184	
U.S. PATENT DOCUMENTS		2132		

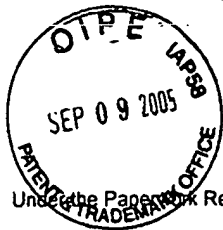
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
ll	US 2003/0104788	June 5, 2003	Kim	455	67.3	
ll	US 2003/0074626	April 17, 2003	Coker et al.	714	752	
ll	US 2003/0023917	January 30, 2003	Richardson et al.	714	749	

FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/OR ABSTRACT
ll	WO 02/103631 A1	December 27, 2002	PCT	G06N	3/00	(N)

OTHER DOCUMENT(S) (Including Author, Title, Data, Pertinent Pages, etc.)	
ll	Mansour et al., "Low-Power VLSI Decoder Architectures for LDPC Codes", Proceedings, IEEE International Symposium on Lower Power Electronics and Design, ISLPED '02, pp. 284-289, August 12-14, 2002
ll	T. Zhang et al., "Joint Code and Decoder Design for Implementation-Oriented (3,k)-Regular LDPC Codes", Proceedings, 35th Asilomar Conference on Signals, Systems, & Computers, pp. 1232-1236, November 4-7, 2001
ll	E. Boutillon et al., "Decoder-First Code Design", Proceedings, International Symposium on Turbo Codes and Related Topics, pp. 459-462, September 4-7, 2000
ll	G. Al-Rawi et al., "Optimizing the Mapping of Low-Density Parity Check Codes on Parallel Decoding Architectures", Proceedings, IEEE Conference on Information Technology: Coding and Computing, pp. 578-586, April 2-4, 2001
ll	A. Selvarathinam et al., "A Massively Scaleable Decoder Architecture for Low-Density Parity-Check Codes", Proceedings, IEEE International Symposium on Circuits and Systems, Vol. 2, pp. 61-64, May 25-28, 2003

EXAMINER	DATE CONSIDERED
ll	10/24/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



PTO/SB/08A (08-03)

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/613,824
	Filing Date	July 3, 2003
	First Named Inventor	Mustafa Erozu
	Art Unit	2133
	Examiner Name	Albert Decady
Sheet 1 of 1	Attorney Docket Number	PD-203009

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	Document Number Number-Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
<i>DE</i>		US 2002/0051501	05-02-2002	Demjanenko, Victor et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Foreign Patent Document Country Code-Number-Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
<i>DE</i>		RICHARDSON, THOMAS J. et al., "Efficient Encoding of Low-Density Parity- Check Codes", IEEE Transactions on Information Theory, Vol. 47, No. 2, February 2001	

Examiner Signature	<i>[Signature]</i>	Date Considered	10/24/05
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